IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

THOMAS GRASSL

SERIAL No.: 09/926,791

FILED: December 20, 2001

GROUP ART UNIT: 2818

EXAMINER: Michael T. TRAN

FOR: SEMICONDUCTOR MEMORY CHIP MODULE

ATTY. REFERENCE: GRAS3005/JEK/JJC

THE COMMISSIONER FOR PATENTS Washington, D.C. 20231

Sir:

Transmitted herewith is an AMENDMENT in the above-identified application.

☐ Small entity status under 37 CFR 1.9 and 1.27 is claimed.

No additional fee is required.

The fee, if any, has been calculated as shown below:



| Fee Basis | Number of Claims After Amendment | Highest Number Previously Paid For | Extra Claims | | Small Entity | Full Fee | |
|---|--|--|--------------|-----|--------------|-----------|--------|
| Total Claims | 9 | - 20 | = | 0 3 | × \$ 9 = | × \$ 18 = | \$0.00 |
| Independent Claims | 1 | - 3 | = | 0 3 | × \$ 42 = | × \$ 84 = | \$0.00 |
| ☐ First Presentation of Proper Multiple Dependent Claim | | | | | + \$140 = | + \$280 = | |
| TOTAL | | | | | | \$0.00 | |

¹ If less than 20 enter 20.

□ Please charge my Deposit Account Number 02-0200 in the amount of \$. A duplicate copy of this sheet is attached. ☐ A check in the amount of \$ is attached. Mark The Commissioner is hereby authorized to charge any additional fees associated with this communication, including fees due under 37 CFR 1.16 and 37 CFR 1.17 or credit any overpayment to Deposit Account Number 02-0200. A duplicate copy of this sheet is attached. ☐ Also enclosed is/are:

BACON & THOMAS, PLLC

625 SLATERS LANE - FOURTH FLOOR ALEXANDRIA, VIRGINIA 223124-1176 (703) 683-0500

23364

PATENT TRADEMARK OFFICE

DATE: March 18, 2003 Respectfully submitted,

JUSTIN J. CASSELL Attorney for Applicant

Registration Number: 46,205

TECHNOLOGY CENTER 2800

² If less than 3 enter 3.

³ If less than 0 enter 0.



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Inventor: Thomas GRASSL

Examiner: Michael T. TRAN

Application No.: 09/926,791

Art Unit: 2818

Filed: December 20, 2001

Attorney No.: GRAS3005/JEK/JJC

For: SEMICONDUCTOR MEMORY CHIP MODULE

AMENDMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

INTRODUCTORY COMMENTS

This is responsive to the Official Action dated December 19, 2002 in the above application. In view of the following amendment and remarks, reconsideration of the application is respectfully requested.

A "List of Current Claims" in the application, showing current amendments to the claims and the status of all claims is presented below.

MAR 19 2003